

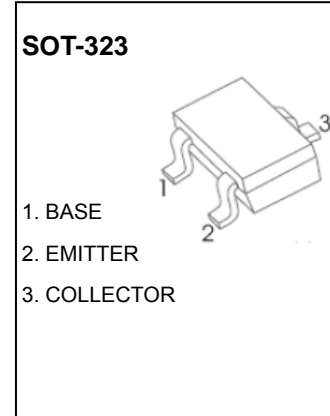
PNP Silicon Epitaxial Planar Transistor

FEATURES

- Large $I_{CMax.} \approx -500mA$
- Low $V_{CE(sat)}$. Ideal for low-voltage operation.
- Complements the 2SC4097.

MAXIMUM RATINGS ($T_a=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-32	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current -Continuous	-500	mA
P_C	Collector Power Dissipation	200	mW
T_J	Junction Temperature	150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance From Junction To Ambient	250	$^\circ C/W$
T_{stg}	Storage Temperature	-55-150	$^\circ C$



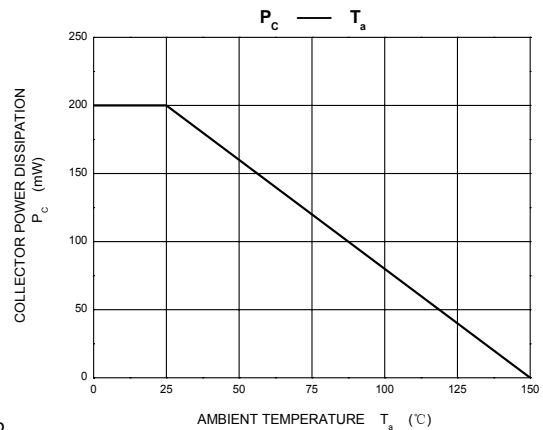
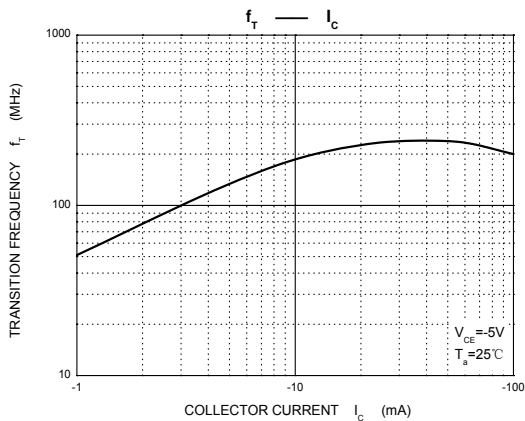
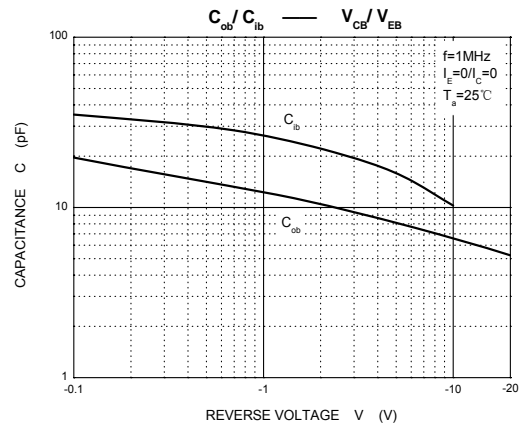
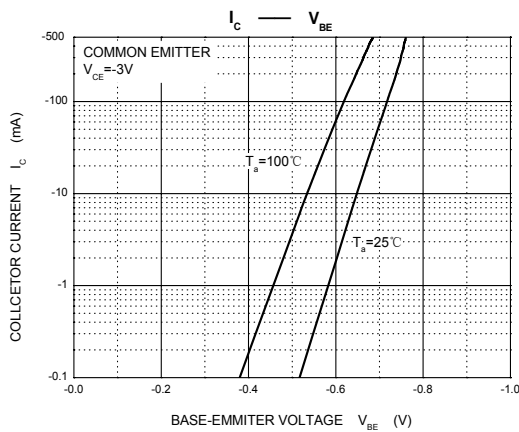
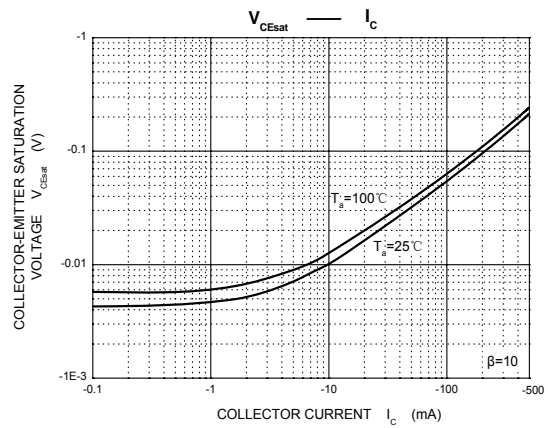
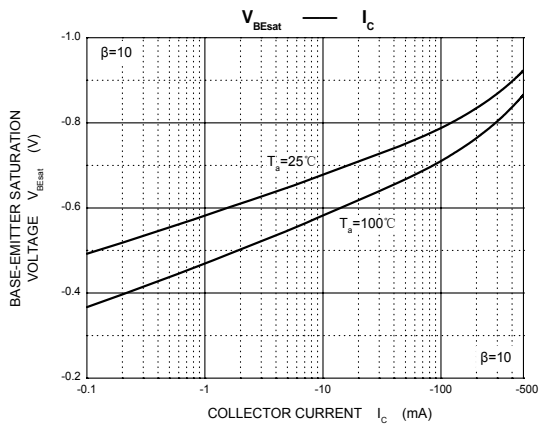
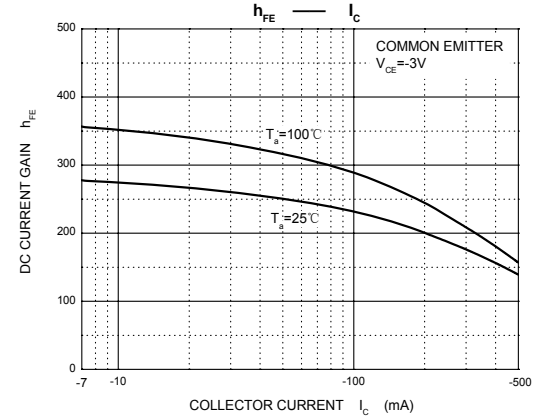
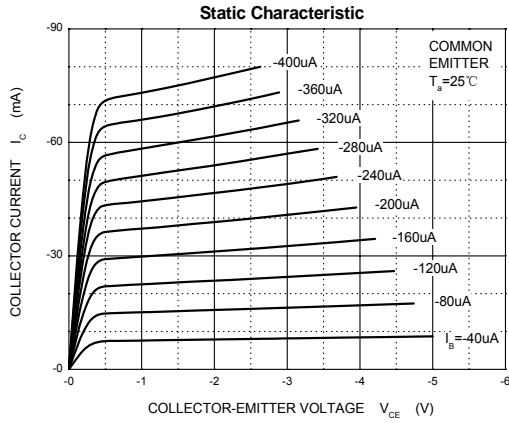
ELECTRICAL CHARACTERISTICS ($T_a=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	$V_{(BR)CBO}$	$I_C = -100\mu A, I_E = 0$	-40			V
Collector-emitter breakdown voltage	$V_{(BR)CEO}$	$I_C = -1mA, I_B = 0$	-32			V
Emitter-base breakdown voltage	$V_{(BR)EBO}$	$I_E = -100\mu A, I_C = 0$	-5			V
Collector cut-off current	I_{CBO}	$V_{CB} = -20V, I_E = 0$			-1	μA
Emitter cut-off current	I_{EBO}	$V_{EB} = -4V, I_C = 0$			-1	μA
DC current gain	h_{FE}	$V_{CE} = -3V, I_C = -10mA$	82		390	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = -100mA, I_B = -10mA$			-0.4	V
Transition frequency	f_T	$V_{CE} = -5V, I_C = -20mA, f = 100MHz$		200		MHz
Collector Output Capacitance	C_{ob}	$V_{CB} = -10V, I_E = 0, f = 1MHz$		7		pF

CLASSIFICATION OF h_{FE}

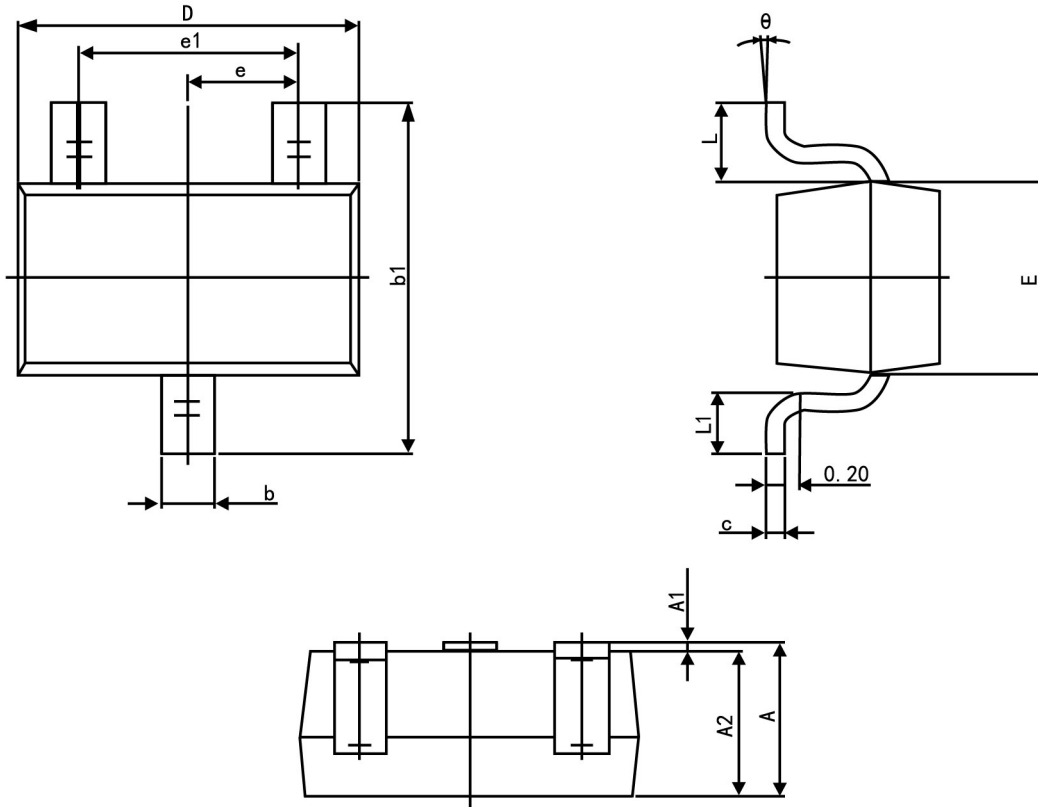
Rank	P	Q	R
Range	82-180	120-270	180-390
MARKING	HP	HQ	HR

Typical Characteristics





SOT-323 Package Outline Dimensions



Symbol	Dimension in Millimeters	
	Min	Max
A	0.900	1.100
A1	0.000	0.100
A2	0.900	1.000
b	0.200	0.400
c	0.080	0.150
D	2.000	2.200
E	1.150	1.350
E1	2.150	2.450
e	0.650 TYP.	
e1	1.200	1.400
L	0.525 REF.	
L1	0.260	0.460
θ	0°	8°